



Single P-Channel 20 V (D-S) MOSFET With Schottky Diode

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}$ (Ω)	I _D (A)			
- 20	0.048 at V _{GS} = - 4.5 V	- 6.3			
	0.068 at V _{GS} = - 2.5 V	- 5.3			
	0.090 at V _{GS} = - 1.8 V	- 4.6			

SCHOTTKY PRODUCT SUMMARY					
V _{KA} (V)	V _f (V) Diode Forward Voltage	I _F (A)			
20	0.48 V at 0.5 A	1			

PowerPAK 1212-8

FEATURES

- TrenchFET® Power MOSFETS: 1.8 V Rated
- ESD Protected: 4500 V
- Ultra-Low Thermal Resistance, PowerPAK® Package with Low 1.07 mm Profile
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



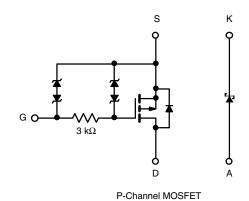
COMPLIANT HALOGEN **FREE**

APPLICATIONS

Charger Switching

3.30 mm 3.30 mm





Ordering Information:

Si7703EDN-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	10 s	Steady State	Unit	
Drain-Source Voltage (MOSFET and Schottky)		V_{DS}	- 20			
Reverse Voltage (Schottky)		V_{KA}	20		V	
Gate-Source Voltage (MOSFET)		V_{GS}	± 12	± 12		
Continuous Drain Current (T, I = 150 °C) (MOSFE	$T_A = 25 ^{\circ}C$	I _D	- 6.3	- 4.3		
Continuous Drain Current (1) = 150 C) (MOSFE	$T_A = 85 ^{\circ}C$		- 4.5	- 3.1		
Pulsed Drain Current (MOSFET)		I _{DM}	- 20		Α	
Continuous Source Current (MOSFET Diode Conduction) ^a		I _S	- 2.3	- 1.1	A	
Average Foward Current (Schottky)		l _F	1			
Pulsed Foward Current (Schottky)		I _{FM}	7			
Maximum Barray Dissipation (MOCFET)	T _A = 25 °C	P _D	2.8	1.3	W	
Maximum Power Dissipation (MOSFET) ^a	$T_A = 85 ^{\circ}C$		1.5	0.7		
Maximum David Dissipation (Cabattle)	T _A = 25 °C	טי	2	1.1	VV	
Maximum Power Dissipation (Schottky) ^a	T _A = 85 °C		1	0.6	1	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	
Soldering Recommendations ^{b,c}			260			

Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. See solder profile (www.vishav.com/doc?73257). The PowerPAK 1212-8 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- c. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.

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THERMAL RESISTANCE RATINGS							
Parameter		Device	Symbol	Typical	Maximum	Unit	
Junction-to-Ambient ^a	t ≤ 10 s	MOSFET	R _{thJA}	35	44		
		Schottky		51	64		
	Steady State	MOSFET		75	94	°C/W	
		Schottky		91	115	C/VV	
Junction-to-Case (Drain)	Steady State	MOSFET	- R _{thJC}	4	5		
		Schottky		10	12		

Notes

a. Surface Mounted on 1" x 1" FR4 board.

Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -800 \mu A$	- 0.45		- 1	V		
Cata Dady Laglage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 1.5	μΑ		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 100	mA		
Zava Cata Valtaga Drain Current	1	V _{DS} = - 20 V, V _{GS} = 0 V	- 1		- 1	4		
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			- 5	- μΑ		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 20			Α		
	R _{DS(on)}	$V_{GS} = -4.5 \text{ V}, I_D = -6.3 \text{ A}$		0.041	0.048	Ω		
Drain-Source On-State Resistance ^a		$V_{GS} = -2.5 \text{ V}, I_D = -5.3 \text{ A}$		0.057	0.068			
		V _{GS} = - 1.8 V, I _D = - 1 A		0.072	0.090			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 6.3 A		14		S		
Diode Forward Voltage ^a	V_{SD}	I _S = - 2.3 A, V _{GS} = 0 V		- 0.8	- 1.2	٧		
Dynamic ^b								
Total Gate Charge	Q_g			12	18			
Gate-Source Charge	Q_{gs}	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -6.3 \text{ A}$		2.5		nC		
Gate-Drain Charge	Q_{gd}			2.9		1		
Turn-On Delay Time	t _{d(on)}			2.5	4			
Rise Time	t _r	V_{DD} = - 10 V, R_L = 10 Ω		4	6			
Turn-Off DelayTime	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 4.5 V, R_G = 6 Ω		15	23	νs		
Fall Time	t _f			12	18	ì		

Notes

b. Guaranteed by design, not subject to production testing.

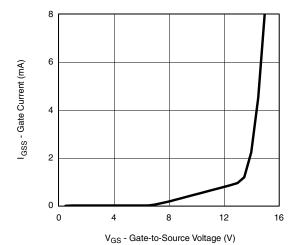
SCHOTTKY SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Тур.	Max.	Unit		
Forward Voltage Drop	V _F	I _F = 0.5 A		0.42	0.48	V	
		I _F = 0.5 A, T _J = 125 °C		0.33	0.4		
Maximum Reverse Leakage Current	I _{rm}	V _r = 20 V		0.002	0.100	mA	
		V _r = 20 V, T _J = 85 °C		0.10	1		
		V _r = 20 V, T _J = 125 °C		1.5	10		
Junction Capacitance	C _T	V _r = 10 V		31		pF	

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

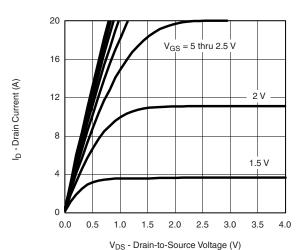
a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$



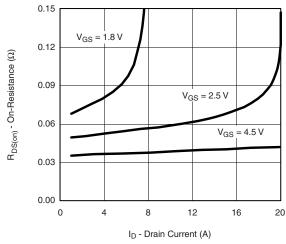
MOSFET TYPICAL CHARACTERISTICS ($T_A = 25~^{\circ}C$, unless otherwise noted)



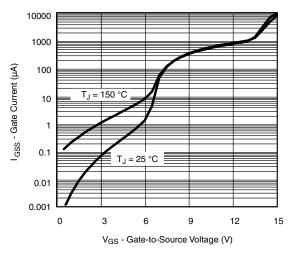
Gate-Current vs. Gate-Source Voltage



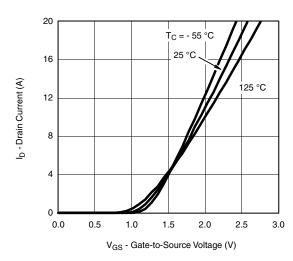
Output Characteristics



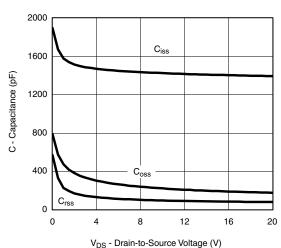
On-Resistance vs. Drain Current



Gate Current vs. Gate-Source Voltage



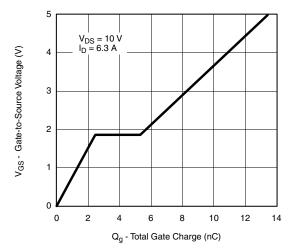
Transfer Characteristics



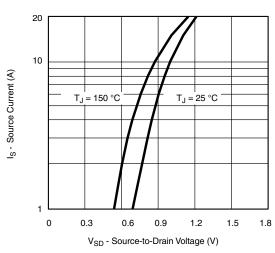
Tos Brain to Course Voltage (

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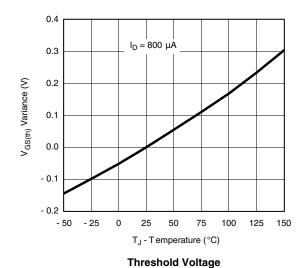
MOSFET TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



Gate Charge

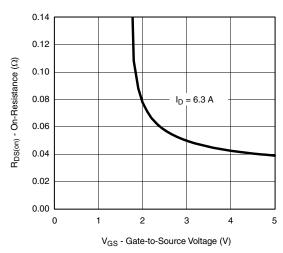


Source-Drain Diode Forward Voltage

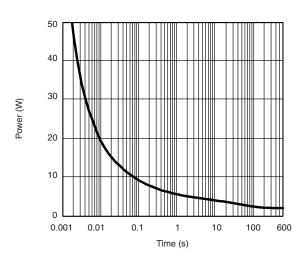


 $V_{GS} = 4.5 V$ $I_D = 6.3 A$ R_{DS(on)} - On-Resistance (Normalized) 1.3 1.1 0.9 - 25 0 150 - 50 25 50 75 100 125 T_J - Junction Temperature (°C)

On-Resistance vs. Junction Temperature



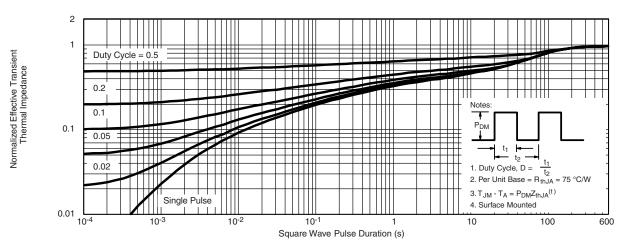
On-Resistance vs. Gate-to-Source Voltage



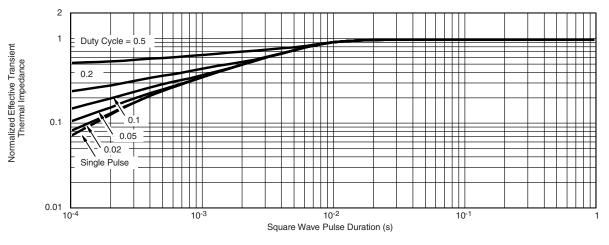
Single Pulse Power, Junction-to-Ambient



MOSFET TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)

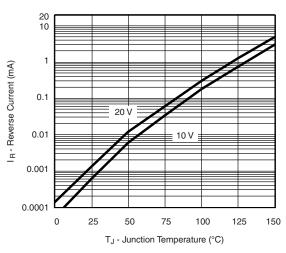


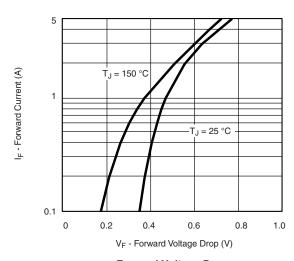
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

SCHOTTKY TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)





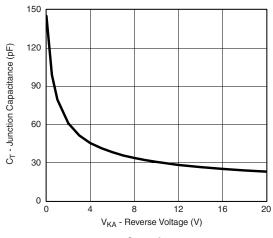
Reverse Current vs. Junction Temperature

Forward Voltage Drop

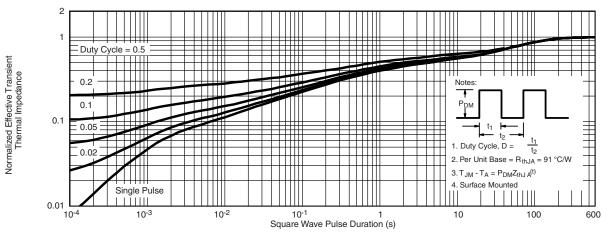
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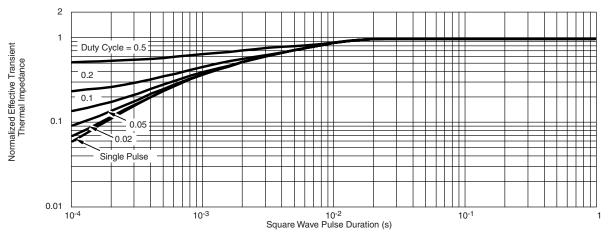
SCHOTTKY TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



Capacitance



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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